

JUN 23 2006

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20872 7590 05/09/2006

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Sheena Hicks	(Depositor's name)
<i>Sheena Hicks</i>	(Signature)
June 23, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/674,085	09/29/2003	Elias Fallon	188122003600	2168
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TITLE OF INVENTION: METHOD FOR GENERATING CONSTRAINED COMPONENT PLACEMENT FOR INTEGRATED CIRCUITS AND PACKAGES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	YES NO	\$700 \$1,400	\$300	\$1,700	08/09/2006
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EXAMINER	ART UNIT	CLASS-SUBCLASS	06/26/2006	TBESHAW2	00000074	031952	10674085
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ROSSOSHEK, YELENA	2825	716-010000	A1 FC;15A1	1400.00	DA
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1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).	2. For printing on the patent front page (37 CFR 1.504)	300.00 DA
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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Cadence Design Systems, Inc.

San Jose, California

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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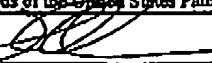
5. Change in Entity Status (from status indicated above)

a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature: Date June 23, 2006Typed or printed name Thomas ChanRegistration No. 51,543

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Number of pages with cover page:	3	
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Preparer of this slip has confirmed that facsimile number given is correct: 12135/srh2

Comments:

OFFICIAL FILING

Attorney Docket No.: 188122003600
 Group Art Unit: 2825
 Examiner: Y. Rossoshek
 Serial No.: 10/674,085
 Filing Date: September 29, 2003
 Inventor: Elias FALLON et al.
 Title: METHOD FOR GENERATING CONSTRAINED
 COMPONENT PLACEMENT FOR INTEGRATED
 CIRCUITS AND PACKAGES

Papers enclosed herewith:

1. Part B - Fee(s) Transmittal + duplicate copy for fee processing - 2 pages

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